

IRIS

NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL

राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल

P.O SRINIVASNAGAR, MANGALORE - 575025

EC Call Letter List

M Tech (Self-Financed), 2023 - 24

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for M Tech (Self-Financed) Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	Komal joshi	MT2023VL0001	VLSI Design
2	Lode Gopi	MT2023VL0002	VLSI Design
3	Aniket chouhan	MT2023VL0003	VLSI Design
4	Aniket chouhan	MT2023SP0001	Signal Processing and Machine Learning
5	MANJUNATH H S	MT2023VL0004	VLSI Design
6	Yogesh Raju N R	MT2023VL0005	VLSI Design
7	Yogesh Raju N R	MT2023SP0002	Signal Processing and Machine Learning
8	Akhil Vaibhav	MT2023VL0006	VLSI Design
9	Marala Bhavvyya sree	MT2023SP0003	Signal Processing and Machine Learning
10	Marala Bhavvyya sree	MT2023VL0007	VLSI Design
11	SANJEEN SUMAN	MT2023VL0008	VLSI Design
12	KEERTHI MARELLA	MT2023VL0009	VLSI Design
13	DAMACHARLA HARSHA VARDHAN	MT2023VL0010	VLSI Design
14	Samyak	MT2023VL0011	VLSI Design
15	Samyak	MT2023SP0004	Signal Processing and Machine Learning
16	Samyak	MT2023CN0001	Communication Engineering and

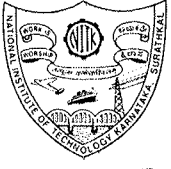


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#	Name	Reference Number	Network Branch/Specialisation
17	Anirudha D Naik	MT2023VL0012	VLSI Design
18	KARTHIK E	MT2023VL0013	VLSI Design
19	KARTHIK E	MT2023SP0005	Signal Processing and Machine Learning
20	Soumik Chakraborty	MT2023VL0014	VLSI Design
21	Michelle Gloria Tauro	MT2023VL0015	VLSI Design
22	Shatabhisa Goswami	MT2023VL0016	VLSI Design
23	Karan G	MT2023VL0017	VLSI Design
24	Noor Tasheel Ahmed	MT2023VL0018	VLSI Design
25	Noor Tasheel Ahmed	MT2023CN0003	Communication Engineering and Network
26	Noor Tasheel Ahmed	MT2023SP0006	Signal Processing and Machine Learning
27	ANJALI L	MT2023VL0019	VLSI Design
28	Sushmitha R Naik	MT2023CN0004	Communication Engineering and Network
29	Sushmitha R Naik	MT2023SP0007	Signal Processing and Machine Learning
30	RATANALA VENKATA RAMANA	MT2023VL0020	VLSI Design
31	DEVENDRA SINGH	MT2023VL0021	VLSI Design
32	ANANDU V P	MT2023VL0022	VLSI Design
33	Chandra Prabha K S	MT2023VL0023	VLSI Design
34	PALLAVI P	MT2023VL0024	VLSI Design
35	L G Naveen Kumar	MT2023VL0025	VLSI Design
36	L G Naveen Kumar	MT2023SP0008	Signal Processing and Machine Learning

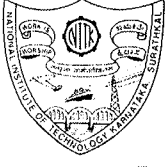


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37	Sushmitha R Naik	MT2023VL0026	VLSI Design
38	Shukla Jehan Chinmay	MT2023VL0027	VLSI Design
39	Shukla Jehan Chinmay	MT2023SP0009	Signal Processing and Machine Learning
40	Shukla Jehan Chinmay	MT2023CN0005	Communication Engineering and Network
41	Sadashiv Samantray	MT2023VL0028	VLSI Design
42	Sadashiv Samantray	MT2023SP0010	Signal Processing and Machine Learning
43	Sadashiv Samantray	MT2023CN0006	Communication Engineering and Network
44	Divyam Satle	MT2023VL0029	VLSI Design
45	Bhogi Ruthwik	MT2023VL0030	VLSI Design
46	Sai Siddartha	MT2023VL0031	VLSI Design
47	Sai Siddartha	MT2023SP0011	Signal Processing and Machine Learning
48	Sai Siddartha	MT2023CN0007	Communication Engineering and Network
49	VARSHA D	MT2023SP0012	Signal Processing and Machine Learning
50	VARSHA D	MT2023CN0008	Communication Engineering and Network
51	Ijaz Ahmed	MT2023VL0032	VLSI Design
52	Aniket chouhan	MT2023CN0009	Communication Engineering and Network
53	Gagan G Honnavarkar	MT2023VL0033	VLSI Design
54	Gagan G Honnavarkar	MT2023CN0010	Communication Engineering and Network
55	Gagan G Honnavarkar	MT2023SP0013	Signal Processing and Machine Learning

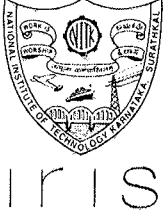


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#	Name	Reference Number	Learning Branch/Specialisation
56	MP Himanshu Dongre	MT2023VL0034	VLSI Design
57	SWAPNA H	MT2023SP0015	Signal Processing and Machine Learning
58	SWAPNA H	MT2023VL0036	VLSI Design
59	SWAPNA H	MT2023CN0011	Communication Engineering and Network
60	Gopika B M	MT2023VL0037	VLSI Design
61	Ritik Kumar Pandey	MT2023VL0038	VLSI Design
62	Ritik Kumar Pandey	MT2023SP0016	Signal Processing and Machine Learning
63	APARNA M S	MT2023VL0039	VLSI Design
64	Gopika B M	MT2023CN0013	Communication Engineering and Network
65	CHAMARTY SAMEERA TANOOJ	MT2023VL0040	VLSI Design
66	Sonal Shashikanth	MT2023CN0014	Communication Engineering and Network
67	Sonal Shashikanth	MT2023SP0018	Signal Processing and Machine Learning
68	Darakshan Imam	MT2023VL0042	VLSI Design
69	Darakshan Imam	MT2023SP0019	Signal Processing and Machine Learning
70	Darakshan Imam	MT2023CN0016	Communication Engineering and Network
71	Ritwik dasmohapatra	MT2023VL0043	VLSI Design
72	kaitepalli rajeswara dattatreya sarma	MT2023VL0044	VLSI Design
73	kaitepalli rajeswara dattatreya sarma	MT2023CN0017	Communication Engineering and Network



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#	Name	Reference Number	Branch/Specialisation
74	SUHAS K SIDDAMAL	MT2023VL0045	VLSI Design
75	RADHIKA RAGHAV	MT2023VL0046	VLSI Design
76	Jahnavi M	MT2023VL0047	VLSI Design
77	Jahnavi M	MT2023CN0018	Communication Engineering and Network
78	Jahnavi M	MT2023SP0020	Signal Processing and Machine Learning
79	SUSARLA LAKSHMI PRASANNA SRAVYA	MT2023VL0048	VLSI Design
80	KEERTHI MARELLA	MT2023SP0021	Signal Processing and Machine Learning
81	KEERTHI MARELLA	MT2023CN0019	Communication Engineering and Network
82	kopparthi dinesh lingaiah	MT2023VL0049	VLSI Design
83	Sharvari Navada A	MT2023CN0020	Communication Engineering and Network
84	Sharvari Navada A	MT2023SP0022	Signal Processing and Machine Learning
85	Koushik	MT2023VL0053	VLSI Design
86	Koushik	MT2023SP0024	Signal Processing and Machine Learning
87	ANIKET KUMAR	MT2023VL0054	VLSI Design
88	MAHAVAR KARTIKKUMAR HIRALAL	MT2023VL0055	VLSI Design
89	FATHIMA SANA P	MT2023VL0056	VLSI Design
90	KUNCHA AKHILESH KUMAR	MT2023VL0058	VLSI Design
91	KUNCHA AKHILESH KUMAR	MT2023VL0059	VLSI Design
92	Anubhav Pal	MT2023VL0060	VLSI Design



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#	Name	Reference Number	Branch/Specialisation
93	Anubhav Pal	MT2023CN0023	Communication Engineering and Network
94	KHUSHBOO WARDHANI	MT2023VL0061	VLSI Design
95	KHUSHBOO WARDHANI	MT2023CN0024	Communication Engineering and Network
96	KHUSHBOO WARDHANI	MT2023SP0027	Signal Processing and Machine Learning
97	shubham kumar ojha	MT2023VL0062	VLSI Design
98	shubham kumar ojha	MT2023CN0025	Communication Engineering and Network
99	shubham kumar ojha	MT2023SP0028	Signal Processing and Machine Learning
100	YASHWANT KUMAR	MT2023VL0063	VLSI Design
101	YASHWANT KUMAR	MT2023SP0029	Signal Processing and Machine Learning
102	KOPPINEEDI NARESH KUMAR	MT2023VL0065	VLSI Design
103	SATWIK PAL	MT2023VL0066	VLSI Design
104	AGHERA HIT AVINASHBHAI	MT2023VL0067	VLSI Design
105	KALAGONI NAGA SAI SANTOSH	MT2023CN0027	Communication Engineering and Network
106	Battula krishna tejaswini	MT2023VL0068	VLSI Design
107	Sunil	MT2023VL0069	VLSI Design
108	SUNIL KUMAR	MT2023VL0070	VLSI Design
109	NAEEM ZAKI R V	MT2023VL0071	VLSI Design
110	NAEEM ZAKI R V	MT2023SP0032	Signal Processing and Machine Learning
111	vineeth n	MT2023VL0072	VLSI Design
112	vineeth n	MT2023CN0029	Communication Engineering and Network



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#	Name	Reference Number	Network Branch/Specialisation
113	Ranchi chourasiya	MT2023VL0073	VLSI Design
114	ANUP KUMAR	MT2023VL0074	VLSI Design
115	Sagar Kashyap	MT2023VL0075	VLSI Design
116	SATWIK PAL	MT2023CN0032	Communication Engineering and Network

K. Srinivas 20/7/23

Head Of Department I/c

Electronics and Communication Engineering

प्राध्यापक एवं विभागाध्यक्ष / HOD
इ. एवं सी. विभाग / Department of E & C
एन.आई.टी.के. सुरत्कल/NITK Surathkal
मंगलूर / MANGALURU - 575 025

Dean Academics

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL**

P.O. SRINIVASNAGAR, MANGALURU-575 025
Telephone: 0824-2473046, Website: www.ece.nitk.ac.in

Date: 19-07-2023

**Shortlisted candidates for MTech self-financed programme–Written Aptitude Test & Interview for the
Year 2023-24**

With reference to your application for admission to MTech self-financed programme in the department of Electronics and Communication Engineering, you are requested to appear for the Written test and Interview at ECE Dept., NITK Surathkal. You should produce all the original records such as Date of Birth Certificate, GATE Score Card (those who have), Degree Certificate and Marks Cards of all semesters (UG programme), SC/ST/OBC/EWS certificate (if applicable as per proforma), Person with disability certificate (if applicable), Conduct Certificate and valid photo identity card. Please keep a self-attested photocopy of all these certificates readily available at the time of interview.

Department	: Department of Electronics and Communication Engineering
Place of Reporting	: Department of Electronics and Communication Engineering, NITK Surathkal
Written Test Date and Time	: August 01, 2023 10.30 AM at ECE Department (Offline)
Announcement of shortlisted candidates for interview	: August 01, 2023, 12.00 PM
Document Verification	: August 01, 2023, 11.30 AM onwards
Interview Date and Time	: August 01, 2023, 2.00 PM Onwards, Meeting room, ECE Department August 02, 2023, 9.00 AM Onwards, Meeting room, ECE Department

NOTE:

1. Candidates should be prepared to appear for the written Aptitude Test before the interview. Fee Structure for M-Tech self-financed programme and Course syllabus are provided on Institute's website, i.e. www.nitk.ac.in. A copy of the syllabus for the aptitude test is given in a separate page.
2. Candidates who have not submitted marks of final examination along with application form shall produce the same at the time of admission if available.
3. Your candidature for this test is provisional & is subject to your fulfilling the educational qualifications & other criteria prescribed for the programme as mentioned in the Information Brochure, failing which your candidature can be summarily rejected after verification/scrutiny at a later stage.
4. **Please keep the Admit Card (Hard copy) ready during the offline test and interview.** You are responsible for safe custody of the Admit Card and in the event of any other person using this Admit Card, the responsibility lies on you to prove that you have not used the service of an impersonator.
5. Please note that no expenses shall be payable for appearing in the written test and Interview.
6. Mobile phones are not allowed inside the exam hall. Candidates can use scientific calculator.

U-Sudhak 20/7/23
Head of the Department

प्राध्यापक एवं विभागाध्यक्ष/PROF & HEAD
इ. एवं सी. विभाग / Department of E & C
एन.आई.टी.के. सुरत्कल/NITK Surathkal
मंगलूर / MANGALURU - 575 025

Date: 20-07-2023

Syllabus for M. Tech. (Self-financed) Aptitude Test July-August 2023

The Test paper has 2 Parts: **Part-1 is compulsory**, and Part-2 is stream specific modules. The candidate is supposed to attempt Module A or Module B or Module C from Part-2 depending on the candidature for a particular M.Tech (Self-financed) streams. Part-1 has 15 multiple choice type questions, whereas, each module of Part-2 has 15 multiple choice type questions. Each correct answer carries 1 mark and wrong answer carries -0.25 marks.

Note: Total duration of Exam is one hour. Scientific calculator is permitted. Mobile phones are prohibited.

Part-1 (Compulsory):

Linear Algebra, Calculus, Differential and Difference equations. Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory.

Part-2 (Stream Specific):

Module-A (CEN Stream)

Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Fundamentals of Signal Processing.

Module-B (SPML Stream)

SP Fundamentals: Time domain analysis of discrete-time systems - Basic discrete time signals, discrete-time Fourier Series, Z Transform – definition and properties, Discrete-time Fourier Series and its properties, Properties and applications of DTFT. Relationship between time, Z and frequency domains, DFT fundamentals and Properties, Design and applications of FIR and IIR filters.

Data Structure Fundamentals: Algorithm analysis, Asymptotic notations. Divide and Conquer algorithms, Analysis of divide and conquer algorithms & examples, Merge sort, quick sort, binary search, Data structures, Linked list, stacks and queues.

Module-C (VLSI Design Stream)

Linear and Digital ICs, Digital System Design, VLSI Technology, CMOS VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.

